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Substitute for form 1449A/B/PTO				<i>Complete if Known</i>	
				Application Number	Not Yet Assigned
				Filing Date	18 AUG 2003
				First Named Inventor	Howard E. Rhodes
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	3	Attorney Docket Number	M4065.0103/P103-A

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
SJ	AA	*4,374,700	02/1983	Scott et al.	
	AB	*5,319,604	06/1994	Imondi et al.	
	AC	*5,461,425	10/1995	Fowler et al.	
	AD	*5,471,515	11/1995	Fossum et al.	
	AE	*5,541,402	07/1996	Ackland et al.	
	AF	*5,576,763	11/1996	Ackland et al.	
	AG	*5,612,799	03/1997	Yamazaki et al.	
	AH	*5,614,744	03/1997	Merrill	
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SJ	AS	*6,396,539	05/2002	Heller et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
SJ	CA	*Fossum, E., "Low Power Camera-on-a-Chip Using CMOS Active Pixel Sensor Technology," 1995 IEEE, pps. 74-77.			
SJ	CB	*Fossum, E., "Architectures for Focal Plane Image Processing," Optical Engineering, Vol. 28, No 8, August 1989, pps. 865-871.			

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S. Murphy

10/14/02

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GJ	CC	*Janesick, J., et al., "New advancements in charge-coupled device technology - sub-electron noise and 4096x4096 pixel CCDs," Proc. SPIE Vol. 1242, 1990, pps. 223-237.		T ²
/	CD	*Kemeny, S.E., et al., "Update on focal-plane image processing research," Proc. SPIE Vol. 1447, 1991, pps. 243-250.		
/	CE	*Mendis, S., et al., "CMOS Active Pixel Image Sensor," IEEE Transactions on Electron Devices, Vol. 41, No. 3, March 1994, pps. 452-453.		
/	CF	*Mendis, S.K., et al., "A 128 x 128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems," 1993 IEEE, pps. 583-586.		
/	CG	*Mendis, S.K., et al., "CMOS Active Pixel Image Sensors for Highly Integrated Imaging Systems," IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February 1997, pps. 187-197.		
/	CH	*Mendis, S.K., et al., "Design of a Low-Light-Level Image Sensor with On-Chip Sigma-Delta Analog-to-Digital Conversion," Proc. SPIE Vol. 1900, July 1993, pps. 31-39.		
/	CI	*Mendis, S.K., et al., "Low-Light-Level Image Sensor with On-Chip Signal Processing," Proc. SPIE Vol. 1952, November 1993, pps. 23-33.		
/	CJ	*Mendis, S.K., et al., "Progress In CMOS Active Pixel Image Sensors," Proc. SPIE Vol. 2172, May 1994, pps. 19-29.		
/	CK	*Nakamura, J., et al., "CMOS Active Pixel Image Sensor with Simple Floating Gate Pixels," IEEE Transactions on Electron Devices, Vol. 42, No. 9, September 1995, pps. 1693-1694.		
/	CL	*Janesick, J., et al., "New advancements in charge-coupled device technology - sub-electron noise and 4096x4096 pixel CCDs," Proc. SPIE Vol. 1242, 1990, pps. 223-237.		
/	CM	*Kemeny, S.E., et al., "Update on focal-plane image processing research," Proc. SPIE Vol. 1447, 1991, pps. 243-250.		
/	CN	*Mendis, S., et al., "CMOS Active Pixel Image Sensor," IEEE Transactions on Electron Devices, Vol. 41, No. 3, March 1994, pps. 452-453.		
/	CO	*Mendis, S.K., et al., "A 128 x 128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems," 1993 IEEE, pps. 583-586.		
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/	CQ	*Mendis, S.K., et al., "Design of a Low-Light-Level Image Sensor with On-Chip Sigma-Delta Analog-to-Digital Conversion," Proc. SPIE Vol. 1900, July 1993, pps. 31-39.		
/	CR	* Nixon, R.H., et al., "256 x 256 CMOS Active Pixel Sensor Camera-on-a-Chip," IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996, pps. 2046-2050.		
/	CS	* Nixon, R.H., et al., "256x256 CMOS Active Pixel Sensor Camera-on-a-Chip," 1996 IEEE International Solid-State Circuits Conference, pps. 178-179.		
/	CT	* Panicacci, R., et al., "Programmable multiresolution CMOS active pixel sensor," Proc. SPIE Vol. 2654, March 1996, pps. 72-79.		
/	CU	* Panicacci, R.A., et al., "128Mb/s Multiport CMOS Binary Active-Pixel Image Sensor," 1996 IEEE International Solid-State Circuit Conference, pps. 100-101.		
GJ	CV	* Yadid-Pecht, O., et al., "CMOS Active Pixel Sensor Star Tracker with Regional Electronic Shutter," IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February 1997, pps. 285-288.		

10/19/97
Sawyer

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GM	CW	* Yaddid-Pecht, O., et al., "Wide dynamic range APS star tracker," Proc. SPIE Vol. 2654, March 1996, pps. 82-92.			
	CX	* Zamowski, J., et al., "Imaging options expand with CMOS technology," Laser Focus World, June 1997, pps. 125-130.			
	CY	* Zhou, Z., et al., "A Cmos Imager with On-Chip Variable Resolution for Light-Adaptive Imaging," 1998 IEEE International Solid-State Circuits Conference, pps. 174-175.			
	CZ	* Zhou, Z., et al., A Digital CMOS Active Pixel Image Sensor For Multimedia Applications, Proc. SPIE Vol. 2894, September 1996, pps. 282-288.			

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Sanderson

10/14/04